

What is claimed is:

1 1. An electronic device having digital electronic communication capability, the
2 electronic device comprising:
3 a data transceiver;
4 a clock driver with an enable input node;
5 a control circuit to drive the enable input node when the data transceiver is
6 initialized; and
7 a clock receiver circuit having a clock detection circuit to detect the presence
8 of an incoming clock signal;
9 wherein the control circuit comprises an initialization circuit to initialize the data
10 transceiver.

1 2. The electronic device of claim 1 wherein the data transceiver comprises:
2 a voltage mode output driver having an output node; and
3 a data receiver having an input node coupled to the output node of the voltage mode
4 driver.

1 3. The electronic device of claim 1 wherein the data transceiver comprises:
2 a current mode output driver having a differential output node; and
3 a data receiver having a differential input node coupled to the differential output
4 node of the current mode driver.

1 4. The electronic device of claim 1 wherein the initialization circuit comprises an
2 impedance control circuit.

1 5. The electronic device of claim 4 wherein the control circuit is operative to
2 enable the clock driver when the impedance control circuit has initialized an
3 impedance of the data transceiver.

1 6. The electronic device of claim 5 wherein:
2 the data transceiver includes a voltage mode driver having an output
3 impedance; and
4 the impedance initialized by the impedance control circuit is the output impedance of
5 the voltage mode driver.

1 7. The electronic device of claim 5 wherein:
2 the data transceiver includes a current mode driver having at least one
3 termination resistor; and
4 the impedance initialized by the impedance control circuit is the at least one
5 termination resistor.

1 8. The electronic device of claim 1 wherein:
2 the data transceiver includes a variable current source circuit; and
3 the initialization circuit is operative to initialize the variable current source circuit.

1 9. The electronic device of claim 1 wherein:
2 the data transceiver includes a receiver circuit having a variable offset; and
3 the initialization circuit is operable to initialize the variable offset of the receiver
4 circuit.